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31647 7590 10/28/2004

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01/28/2005 YPOLITE2 00000022 090456 10697365

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BRIAN M. DUGAN

(Depositor's name)

Brian M. Dugan
January 27, 2005

(Signature)

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/697,365	10/30/2003	Leendert M. Huisman	BUR920010034US2	7902

TITLE OF INVENTION: USING CLOCK GATING OR SIGNAL GATING TO PARTITION A DEVICE FOR FAULT ISOLATION AND DIAGNOSTIC DATA COLLECTION

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1370	\$300	\$1670	01/28/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
RAYMOND, EDWARD	2857	702-117000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1. ROBERT A. WALSH

2. DUGAN & DUGAN, PC

3.

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INTERNATIONAL BUSINESS
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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

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Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(e)(2).

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Authorized Signature Brian M. DuganDate January 27, 2005Typed or printed name BRIAN M. DUGANRegistration No. 41,720

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FACSIMILE COVER SHEET

January 27, 2005

PLEASE DELIVER TO THE ATTACHED MESSAGE TO:

Examiner Edward Raymond

Phone: (571) 272-2221

Fax: (703) 746-4000

From: Brian M. Dugan

Our File No.: Docket No. BUR920010034US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Leendert M. Huisman et al.
Serial No. : 10/697,365
Filed : October 30, 2003
For : USING CLOCK GATING OR SIGNAL GATING TO
PARTITION A DEVICE FOR FAULT ISOLATION
AND DIAGNOSTIC DATA COLLECTION
Examiner : Edward Raymond
Group Art Unit : 2857

TOTAL NUMBER OF PAGES INCLUDING THIS PAGE: 3

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